

PA-IDC

QUERY CONTROL FORM		RTIS USE ONLY	
Application No. <u>09/146,259</u>	Prepared by <u>NPB</u>	Tracking Number <u>06004963</u>	
Examiner-GAU <u>TRENT - 2183</u>	Date <u>10/6/04</u>	Week Date <u>8/30/04</u>	
	No. of queries <u>1</u>	IFW (PUT3)	

JACKET			
a. Serial No.	f. Foreign Priority	k. Print Claim(s)	p. PTO-1449
b. Applicant(s)	g. Disclaimer	l. Print Fig.	q. PTOL-85b
c. Continuing Data	h. Microfiche Appendix	m. Searched Column	r. Abstract
d. PCT	i. Title	n. PTO-270/328	s. Sheets/Figs
e. Domestic Priority	j. Claims Allowed	o. PTO-892	t. Other

SPECIFICATION	MESSAGE
a. Page Missing	<p>On page 4 of claim pages dated 02/26/01, original claim 17 depends from claim 25. please advise/correct claim dependency.</p> <p>Thank you</p>
b. Text Continuity	
c. Holes through Data	
d. Other Missing Text	
e. Illegible Text	
f. Duplicate Text	
g. Brief Description	
h. Sequence Listing	
i. Appendix	
j. Amendments	
k. Other	
CLAIMS	
a. Claim(s) Missing	<p>RESPONSE</p>
b. Improper Dependency	
c. Duplicate Numbers	
d. Incorrect Numbering	
e. Index Disagrees	
f. Punctuation	
g. Amendments	
h. Bracketing	
i. Missing Text	
j. Duplicate Text	
k. Other	
initials <u>mm</u>	
initials	

15. (Amended) A data processing device according to Claim 14, wherein
said [second] first description held in said [third] first register can be variably set.

16. (Amended) A data processing device according to Claim 14, further
comprising:

a program counter which sequentially counts an address corresponding to each of
the plurality of instructions and holds the address, wherein an address value is held in said
[third] first register as said [second] first description; and

a^3 said instruction execution unit, detects an event that the address value held in said
[third] first register is in agreement with an address of said program counter and starts to
[judge] determine whether or not said condition is satisfied in response to the detection.

17. (Amended) A data processing device according to Claim [15] 25, wherein
said conditional instruction has a field for designating an operation which designate
contents of the operation, a field for designating condition which designates the executing
condition of the operation and a field for designating an amount of delay which designate a
[time] timing for [judging] determining the execution condition;

said instruction decoder produces said first control signal based on contents
described in said field for designating operation, outputs the second description in
accordance with the contents described in said field for designating the condition [as said
first description] and outputs the contents described in said field for designating the amount
of delay;